

Form PTO-1449

U.S. Department of Commerce  
Patent and Trademark OfficeAtty. Docket No.  
P24677Serial No.  
UnknownINFORMATION DISCLOSURE STATEMENT  
BY APPLICANT  
(Use several sheets if necessary)Applicant  
Ryutaro YAMANAKA et al**COPY**Filing Date  
December 31, 2003Group  
Unknown

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6 1 9 5 7 8 2	02/27/01	RAHMATULLAH et al			
	6 2 5 7 7 5 6	07/10/01	ZARUBINSKY et al			
	6 4 7 7 6 6 1	11/05/03	YAMANAKA			
	5 6 3 3 8 9 7	05/27/97	FETTWEIS et al			
	5 9 2 3 7 1 3	07/1999	HATAKEYAMA			
	5 3 2 7 4 4 0	07/1999	FREDRICKSON et al			
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## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
0	9 - 1 4 8 9 4 3	06/06/97	JAPAN			X
6	3 - 2 1 5 2 2 7	09/07/88	JAPAN			X
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## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

1	English Language Abstract JP 09-148943.
2	English Language Abstract JP 63-215227.
3	UEDA et al., "A 16-bit Digital Signal Processor with Specially Arranged Multiply-Accumulator for Low Power Consumption",
	IEICE Trans. Electron., Vol. E78-C, No. 12, December 1995.

EXAMINER

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use several sheets if necessary)		Applicant Ryutaro YAMANAKA et al	
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		Group Unknown	

**U.S. PATENT DOCUMENTS**

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	5 7 4 2 6 2 1	08/1998	AMON et al			
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	5 8 8 1 1 0 6	03/09/99	CARTIER			
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	5 3 7 9 3 0 6	01/1995	NOMA et al			
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	6 1 2 5 1 5 3	09/26/00	SUGISAWA et al			

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DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
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1 1 - 5 5 1 3 0	02/26/99	JAPAN			

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

4	H. LOU, "Viterbi Decoder Design for the IS-95 CDMA Forward Link", IEEE 46th Vehicular Technology Conference, No. 2, 1996, pp.1346-1350.
5	English Language Abstract of JP6-164423.
6	English Language Abstract of JP8-84082.
7	English Language Abstract of JP9-148943.
8	English Language Abstract of JP10-107651.
9	English Language Abstract of 10-209882.
1 0	English Language Abstract of 11-55130.

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